

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
APPLICANT: Bowles et al. EXAMINER: Ray
SERIAL NO: 08/346,834 ART GROUP: 2305
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TITLE: Interrupt Mask Disable Circuit and Method

PATENT

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Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

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GROUP 2300

In response to a telephone interview with Examiner Ray, the applicants are submitting this supplemental amendment.

In the claims:

In claim 6, at line 1 of the claim, please delete the word "first" and insert the word --second--.

In claim 7, at line 1 of the claim, please delete the word "second" and insert the word --first--.

Please amend claim 27 as follows:

- 11
27. (twice amended) A method for masking an interrupt, the method comprising the steps of:
- receiving an interrupt signal, a mask signal, and a mask override signal;

RUN ALT+C MACRO

25